OIPE
FEB 12 2007
PATENT & TRADEMARK OFFICE

FIG. 7

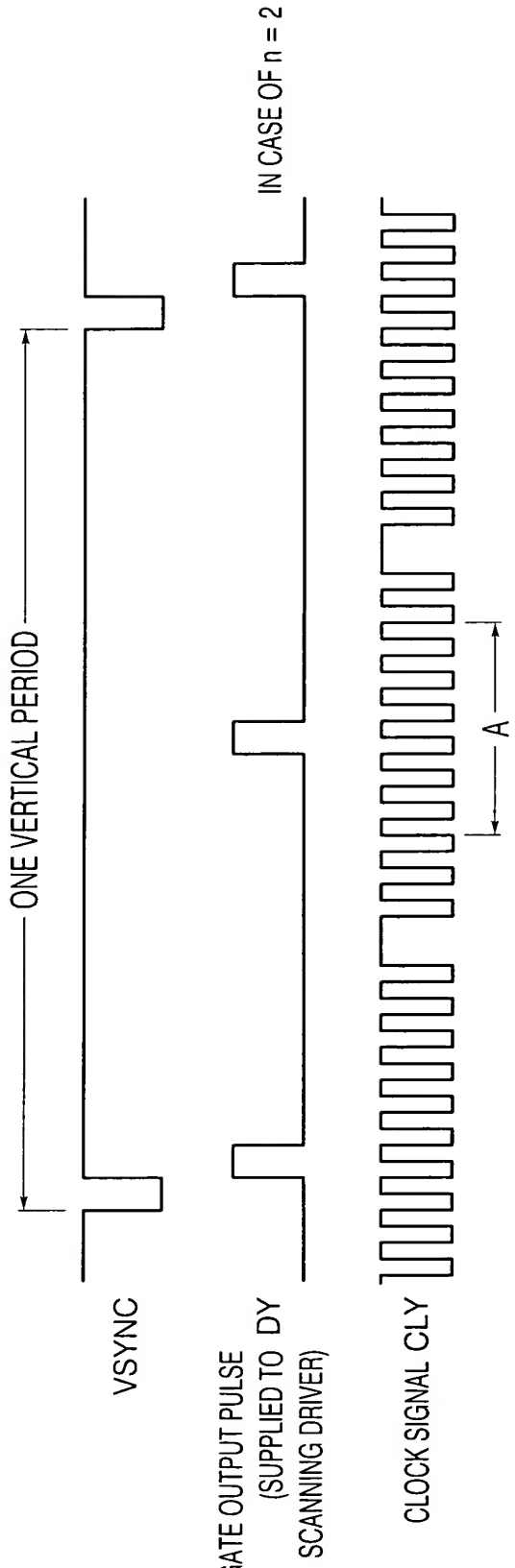
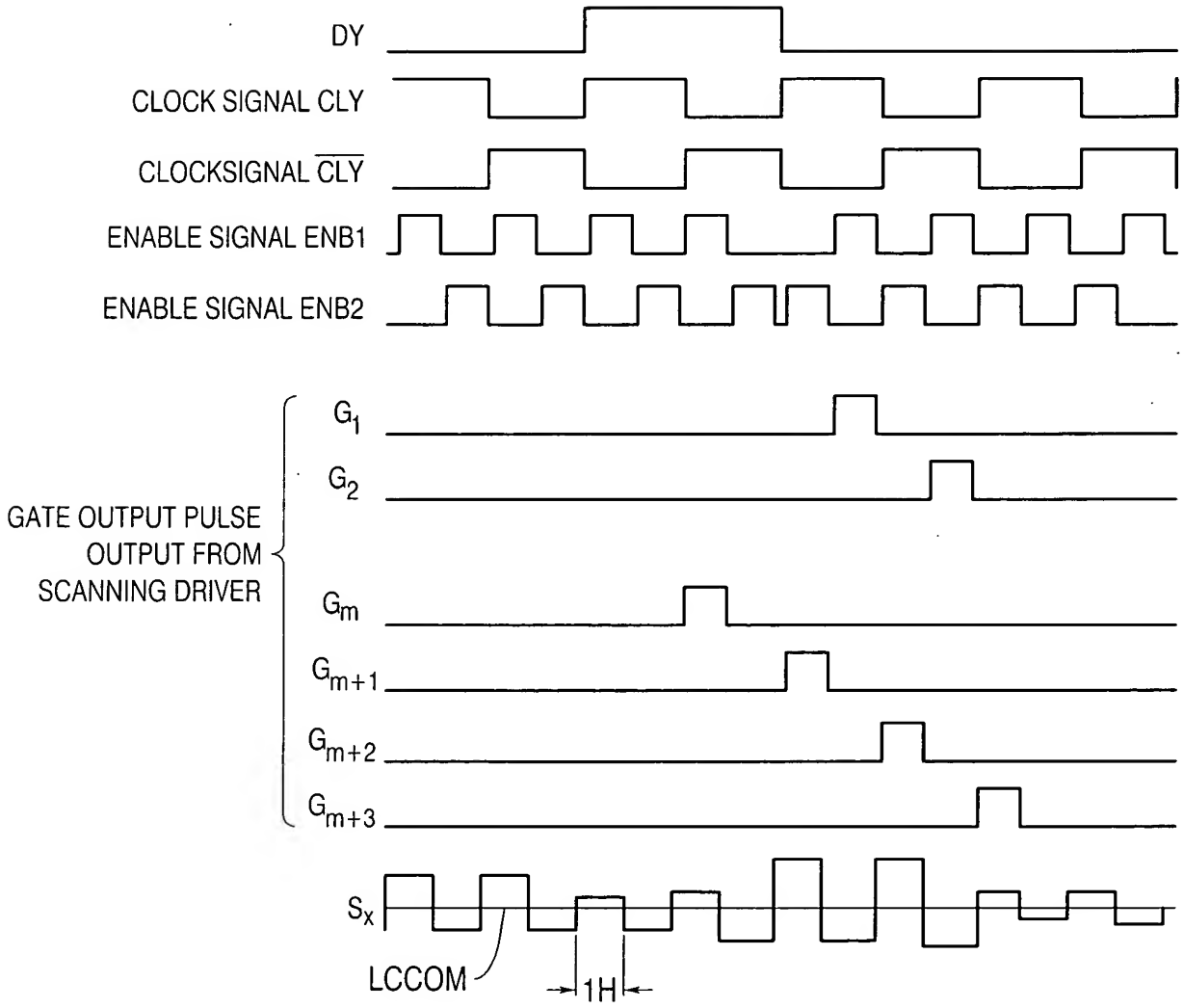


FIG. 8


DY
CLOCK SIGNAL CLY
CLOCKSIGNAL $\overline{CLY}$
ENABLE SIGNAL ENB1
ENABLE SIGNAL ENB2
GATE OUTPUT PULSE OUTPUT FROM SCANNING DRIVER
G_1
G_2
G_m
G_{m+1}
G_{m+2}
G_{m+3}
S_x
LCCOM
1H